TB

DRCT

TERM4_R1_DR

IN

TERM4_R1_TB

IN

EDGE_LFT

TOP_LFT

BOT_LFT

SLOT6_ECL

SEL_TOP_LFT

TOP_LFT_V

BOT_LFT_V

BOT TOP EDGE

SLOT6_ECL

SEL_TOP

R1_CNR_LFT

OUT

TOP_V

BOT_V

EDGE_RHT

TOP_RHT

BOT_RHT

EDGE TOP BOT

SEL_TOP

R1_CNR_RHT

OUT

SEL_TOP_RHT

TOP_V

TOP_RHT_V

BOT_V

BOT_RHT_V

RESET

CLK_BUS

THRSH

TDO

U130

UWHEP EISO-R1

BSCN_CTRL

TP_LF

TDI

ECL_REF1

PCODE

MAX

| Changes | By | Date | Drawn JLL | Project CMS CAL TRIGGER PROCESSOR |
|---|---|---|---|---|
| | | | Date 22/10/01 | EISO CARD |
| | | | CHK'D | BLOCK: EISO_R1 |
| | | | Scale | High Energy Physics |
| REVB | JLL | 31/01/03 | D 0784 | University of Wisconsin |