U77

ADDER ASIC

UWHEP/CMS ADDER-R1

Changes	By	Date	Drawn JLL	Project CMS CAL TRIGGER PROCESSOR
			Date 4/4/01	RECEIVER CARD
			CHK'D	BLOCK: FRST_ADD0
			Scale	High Energy Physics
REVB	JLL	6/12/02	D0722	University of Wisconsin