LOCDATA
BSCN_INST
LOCADD
BSCN_WRT
CMP_PHS
CMDS
RWCNTRL
DEC_ENB
LUT_DATOUT
TRNS_CLK
LUT_ADRS
TRN40
ADRS_DIST
CLK
RST
CNT_CTRL
MEM_ADRS
SELCT_ERR
BX0'
BX0
ERR
PHS
ERR_PHS
ERR_CNTL
LOCDATA
CMP_PHS
SEL_MEM
DATA_OUT
SELB
SEL2
EISODATA
LUTR0
LUTR1
SELWRT
WRTLUTS
LA21
ERR_ENBS
LUTR0
LUTR1
MUX_CNTRL
MUX_SEL
LUTS
SETUPR
SETUPW
R/W
DO NOT INSTALL
ENB_ODD
DLY_CNTL
IO_CNTRL
CNR_CNTRL
EISODATA
ERR_CNTL

Changes	By	Date	Drawn JLL	Project CMS CAL TRIGGER PROCESSOR
			Date 14/1/97	RECEIVER CARD
			CHK'D	BLOCK: TRNSLTRS
			Scale	High Energy Physics
REVB	JLL	6/12/02	D0675	University of Wisconsin