U173

VMEbus Interface Controller

CY7C960A-ASC

CMD_CNTRL

ST_ID

VME_BUF0

VME_BUF1

VME_BUF2

VME_BUF3

DEC_ENB

CLK8Ø

RWCNTRL

BSCN_STAT

BSCN_CNTL

CMP_PHS

CMDS

BSCAN

LOCADD

LOCDATA

IO_CNTRL

ERR_CNTL

Changes	By	Date	Drawn JLL	Project CMS CAL TRIGGER PROCESSOR
			Date 13/12/96	RECEIVER CARD
			CHK'D	BLOCK: VME
			Scale	
				High Energy Physics
REVB	JLL	6/12/02	D 0665	University of Wisconsin
REVA	JLL	1/1/01		